



09/831,413

PATENT
Attorney Docket No. 211178
Client Reference No. F32292US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Michael J. UNDERHILL

Application No. 09/831,413

Filed: August 1, 2001

For: ANTI-JITTER CIRCUITS

Art Unit: 2816

Examiner: M.T. Nguyen

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ABSTRACT OF THE DISCLOSURE

An anti-jitter circuit has an integrator storage capacitor. A charge pump derives from an input pulse train at least one charge packet during each cycle of the input pulse train and supplies the charge packets to the storage capacitor. A controlled current sink operating in conjunction with a high impedance low pass filter continuously discharges the storage capacitor to create a sawtooth voltage waveform having a mean d.c. voltage level. A differential comparator compares the sawtooth voltage waveform with the mean d.c. voltage level and the comparator output is used to trigger a monostable circuit to generate an output pulse train having reduced time jitter